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12-24-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : John A. Wishneusky
Serial No.: 09/989,482
Filed : November 19, 2001
Assignee : Intel Corporation
Title : CONTEXT SCHEDULING

Art Unit: 2182
Examiner:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first Office action on the merits. No fee is required.

Each enclosed document listed on the herewith Form PTO-1449 was cited in the attached International Search Report dated November 25, 2003, issued in a foreign counterpart application not more than three months ago. To the best knowledge of the undersigned, this is the first citation of these items in any

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Typed or Printed Name of Person Signing Certificate

Applicant : John A. Wishneusky
Serial No.: 09/989,482
Filed : November 19, 2001
Assignee : Intel Corporation

Attorney's Docket No.:10559-536001

communication from a foreign patent office in a counterpart
foreign application.

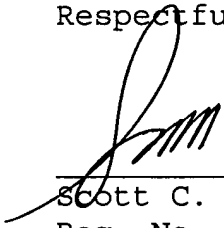
The documents are in the English language; hence no concise
explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the
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Examiner's initials in the left column per MPEP 609 are
earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit
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Respectfully submitted,

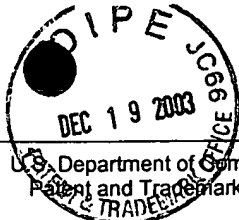
Date: 12/17/03



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Substitute Form PTO-1449
(Modified)U.S. Department of Commerce
Patent and Trademark OfficeAttorney's Docket No.
10559-536001Application No.
09/989,482**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

(37 CFR §1.98(b))

Applicant

John A. Wishneusky

Filing Date

November 19, 2001

Group Art Unit

2182

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
16SK	AA	6,092,180	07/2000	Anderson, et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

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Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
16SK	AL	0 942 366	09/1999	EPO				
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
16SK	AQ	Agarwal, et al., "Sparcle: An Evolutionary Processor Design for Large-Scale Multiprocessor", <i>IEEE Micro</i> , 13(3):48-61 (1993).
	AR	
	AS	
	AT	

Examiner Signature

Ken S. Kim

Date Considered

4-14-04

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.